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## HIGH PERFORMANCE/HIGH VOLTAGE MODE

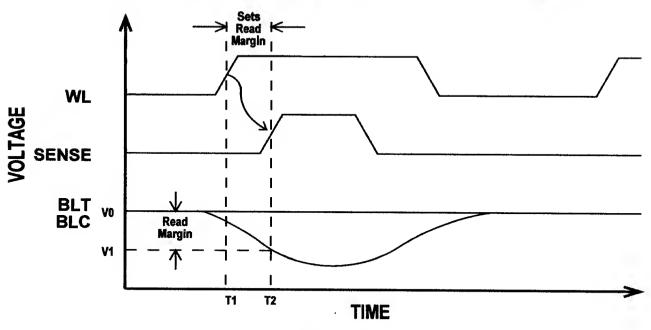


FIG. 2A

## LOW POWER/LOW VOLTAGE MODE

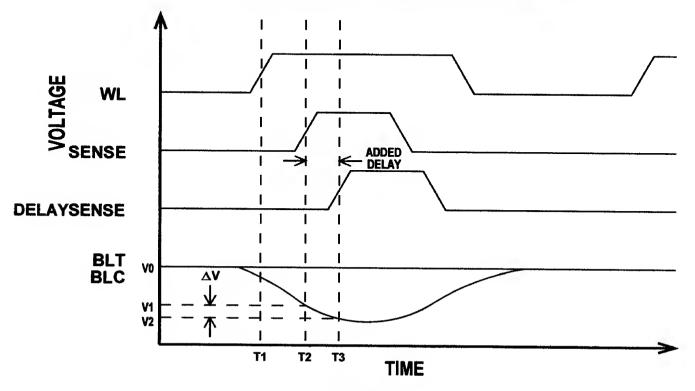
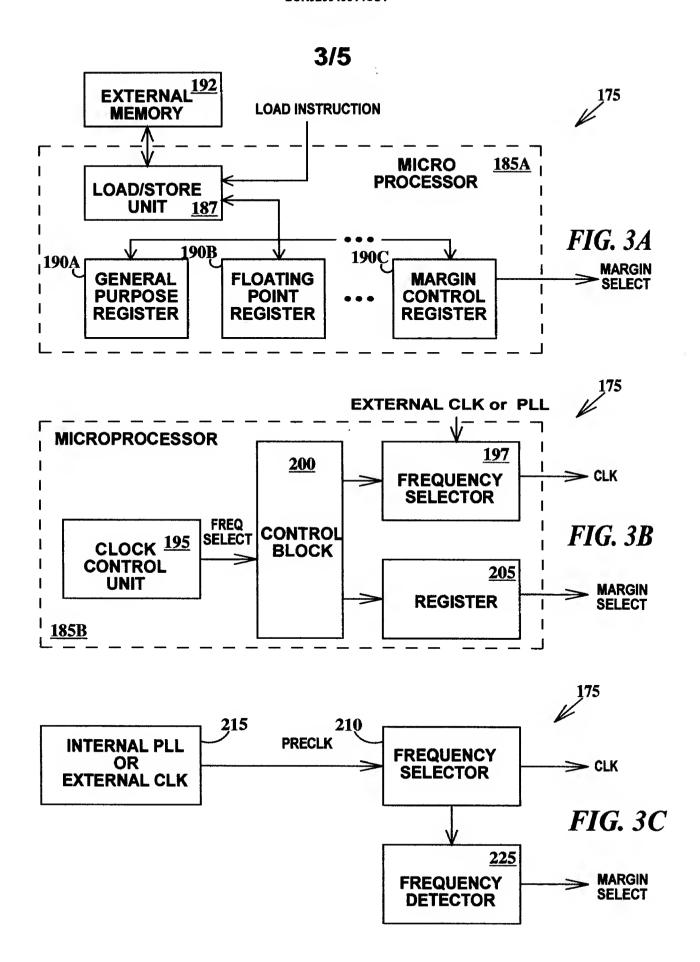
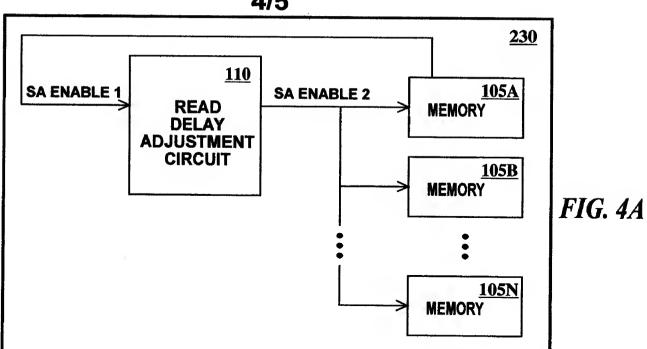
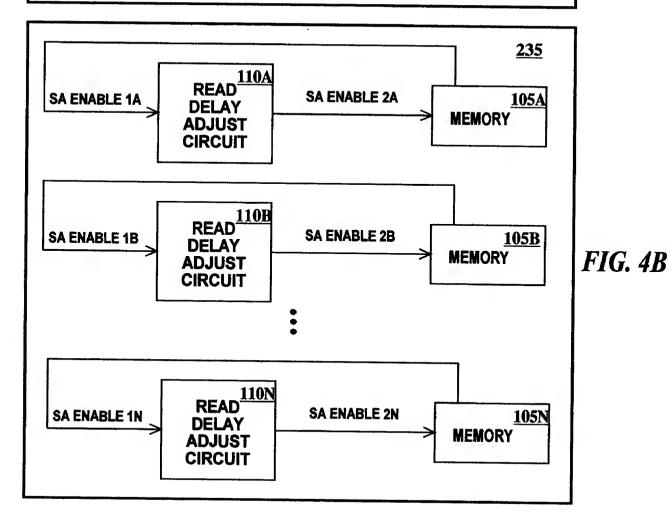


FIG. 2B

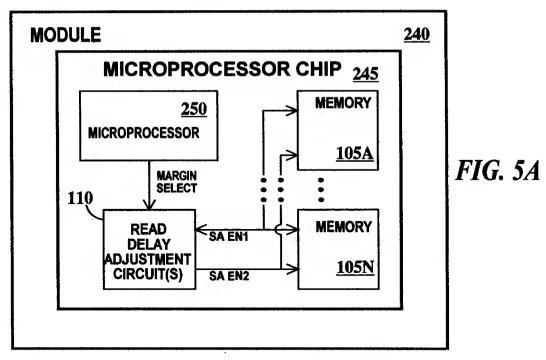








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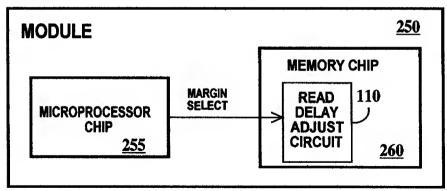
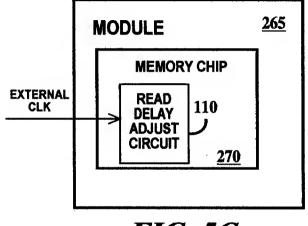


FIG. 5B



MODULE 275

MEMORY CHIP 280

READ CLOCK GEN ADJUST CIRCUIT CLK 285

FIG. 5C FIG. 5D